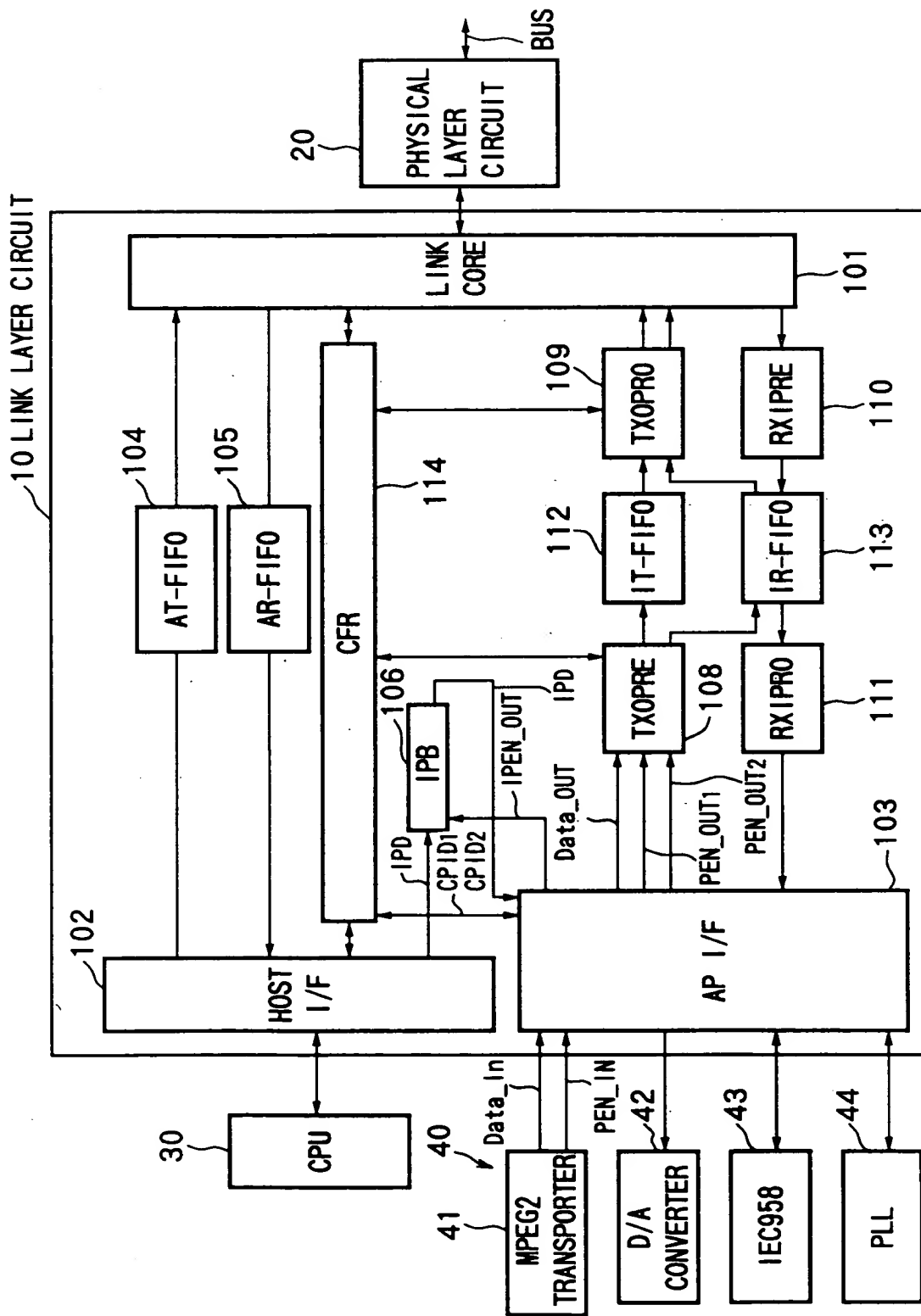
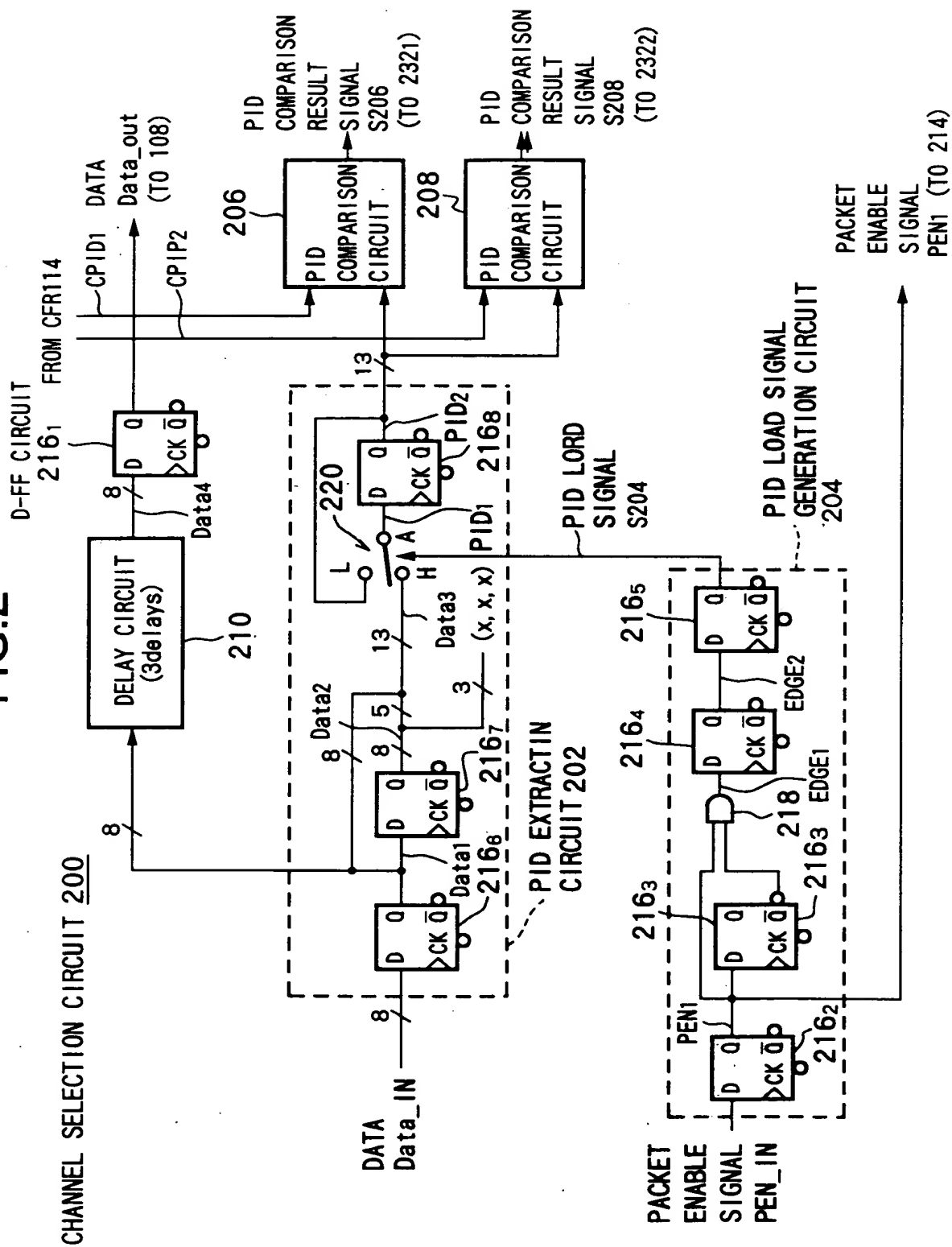


FIG.1



**FIG. 2**





**FIG.4A CK**

CK

**FIG.4B** PEN\_IN

PEN\_IN

**FIG.4C** Data\_IN

## Data IN

**FIG.4D** **PEN<sub>1</sub>**

**PEN<sub>1</sub>**

**FIG.4E**    Data1

## Data1

**FIG.4F**    **Data2**

## Data2

**FIG.4G** **EDGE<sub>1</sub>**

EDGE<sub>1</sub>

**FIG.4H** **EDGE2**

## EDGE2

**FIG.4I**      **S204**

**S204**

**FIG.4J** PID<sub>1</sub>  
(Data3)

PID<sub>1</sub>  
(Data3)

FIG. 4K

## PID2

**FIG.4L S206**

**S206**

**FIG.4M** PEN\_OUT<sub>1</sub>

PEN OUT<sub>1</sub>

**FIG.4N S208**

**S208**

**FIG.40** PEN\_OUT<sub>2</sub>

**PEN\_OUT2**

**FIG.4P DATA\_OUT**

DATA\_OUT

**FIG.4Q SW**

SW

**FIG.4R**  $\text{PEN}_2$

**PEN<sub>2</sub>**

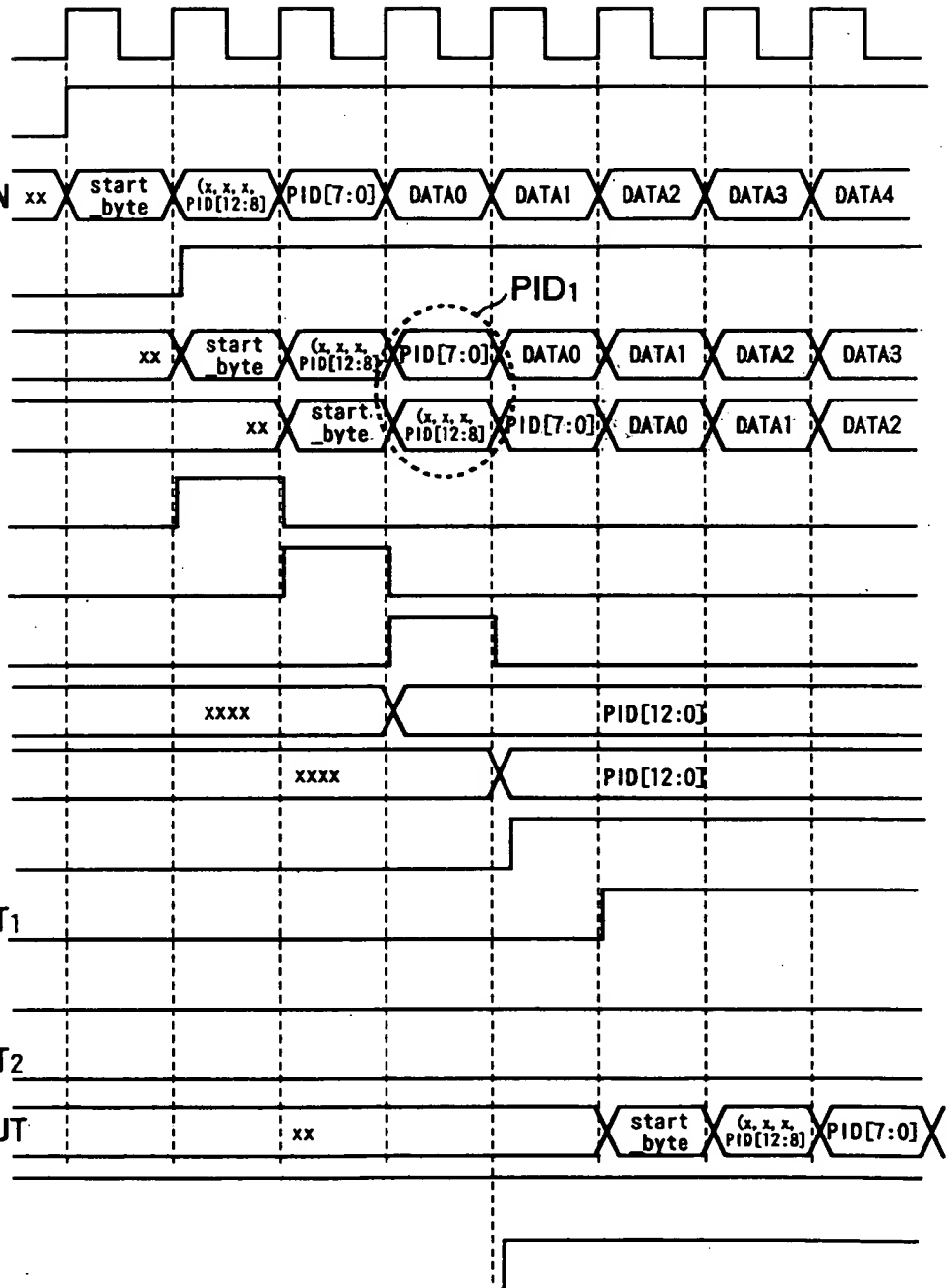


FIG.5

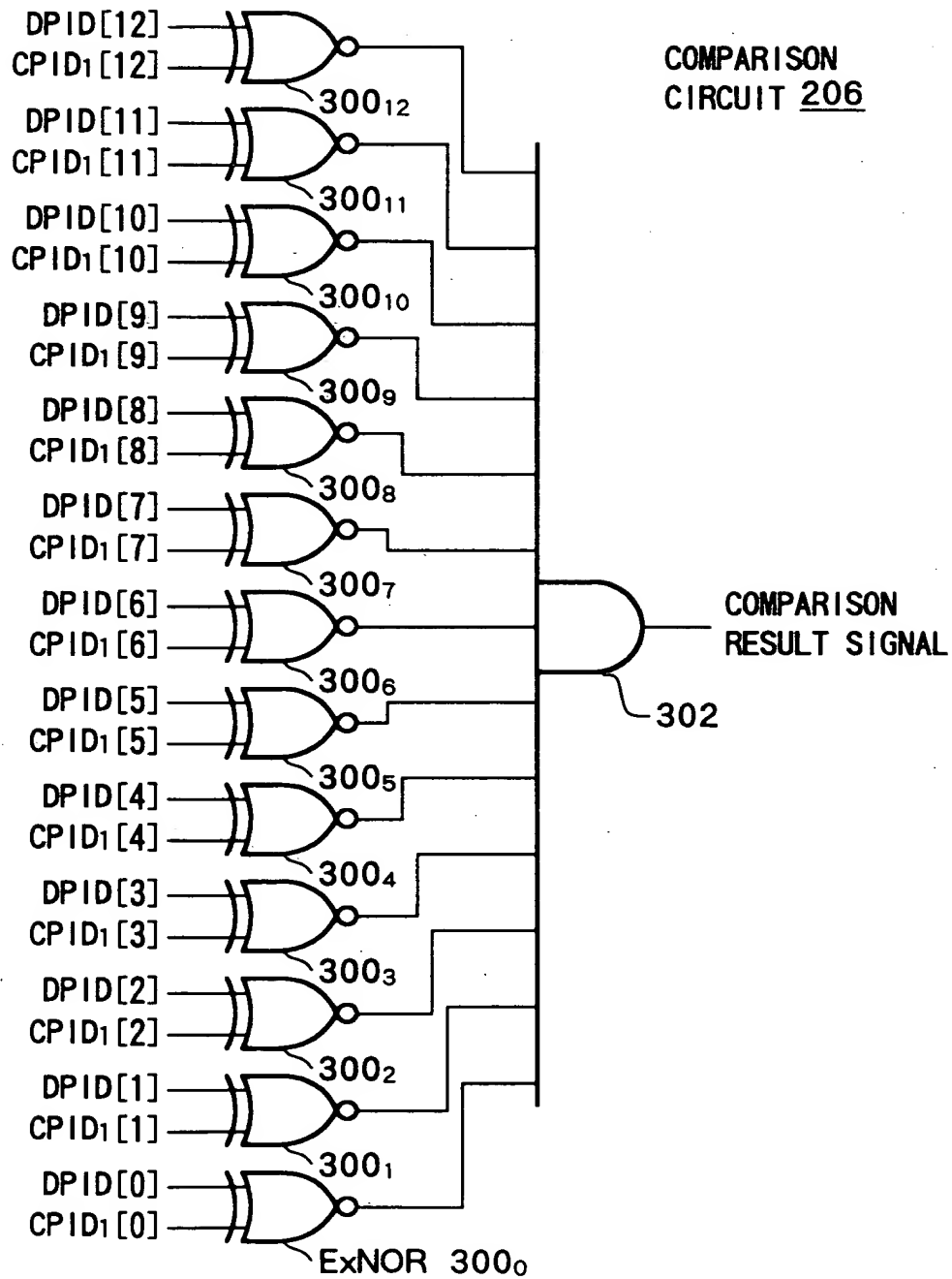


FIG.6

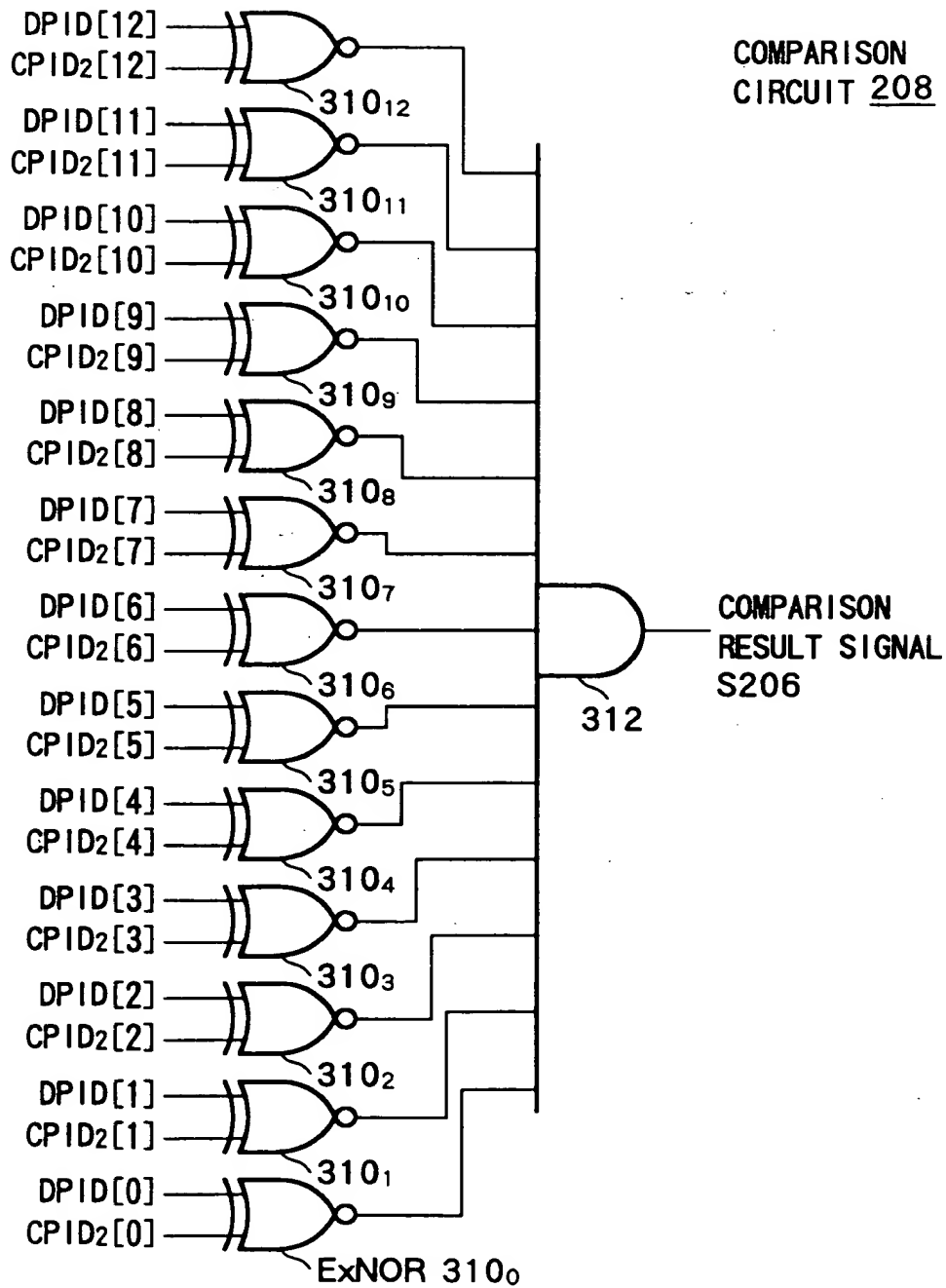


FIG. 7

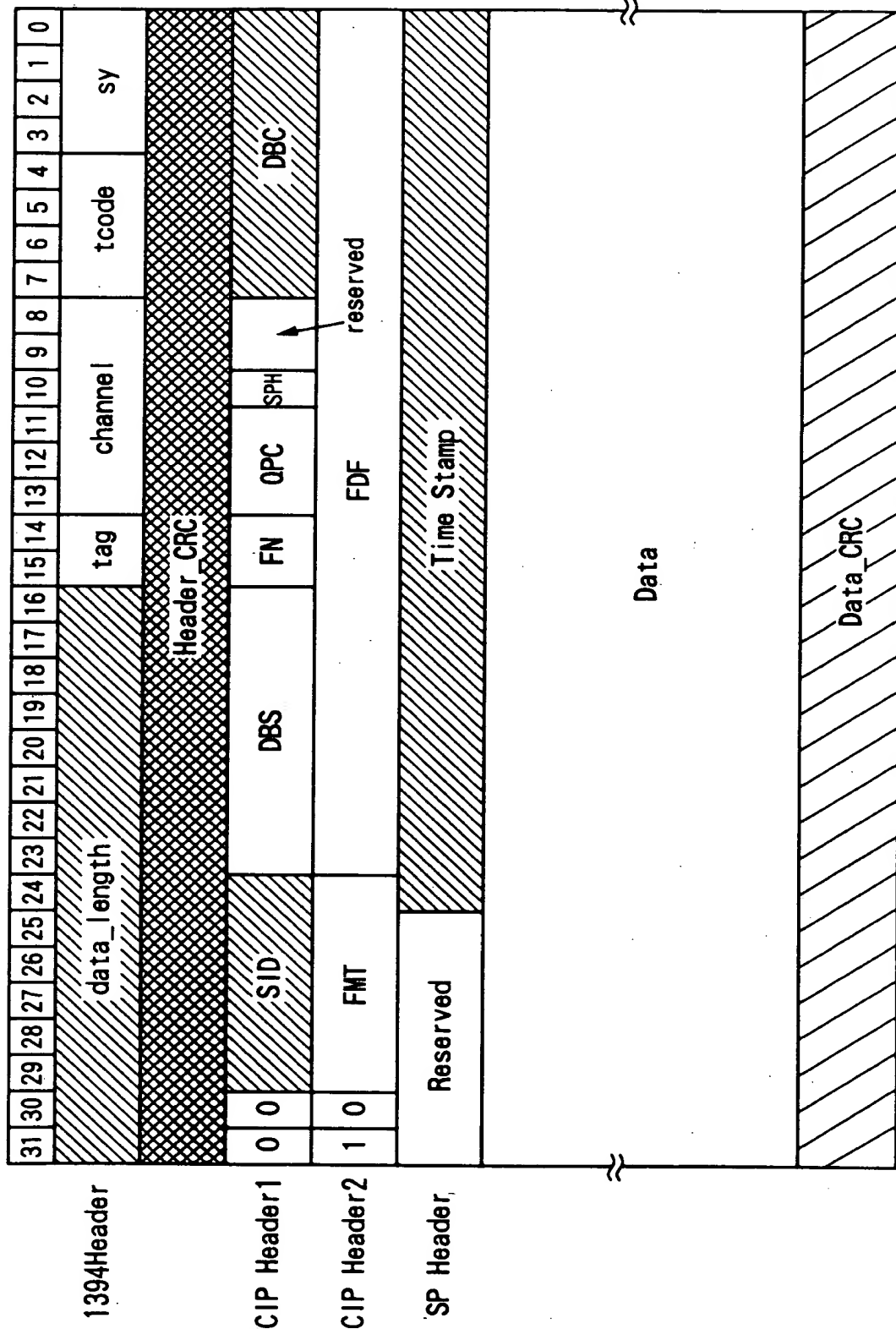


FIG.8

IEEE1394BUS

